

Decomposition of finite-valued streaming string transducers

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Transformations of objects, here **words**

transduction = function or relation between words

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transduction = function or relation between words

hannover	→	{hannover}*	Kleene iteration
hannover	→	revonnah	mirror
hannover	→	hannover hannover	duplicate
hannover	→	over hann	split & swap

MSOT = monadic second-order transductions [Courcelle '95]

Logically define the output inside copies of the input:





- ❖ **domain:** unary formula selecting positions in each copy
- ❖ **order:** binary formula defining an order on the domain
- ❖ **letters:** unary formulas partitioning the domain

hannover \longrightarrow revonnaah

mirror

$\varphi_{<}(x,y) = "x > y"$ // $x < y$ in the output iff $x > y$ in the input

Finite-state Transducers = automata with outputs on transitions

	Deterministic	Non-deterministic
One-way	1DFT 	1NFT 
Two-way	2DFT 	2NFT 

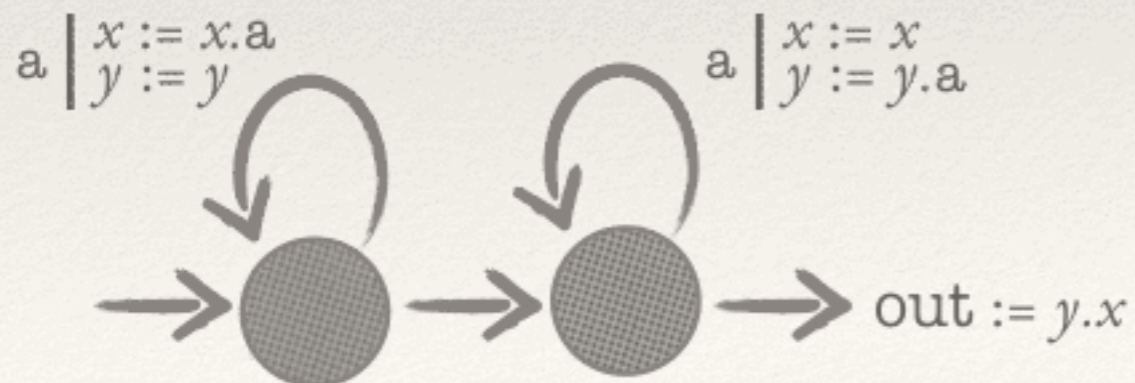
SST = Streaming String Transducers

[Alur, Cerny '10]

- ❖ deterministic / non-deterministic
- ❖ 1-way
- ❖ write-only registers to store partial outputs
+ copyless restriction = each register used at most once

E.g. split & swap

$x =$
 $y =$



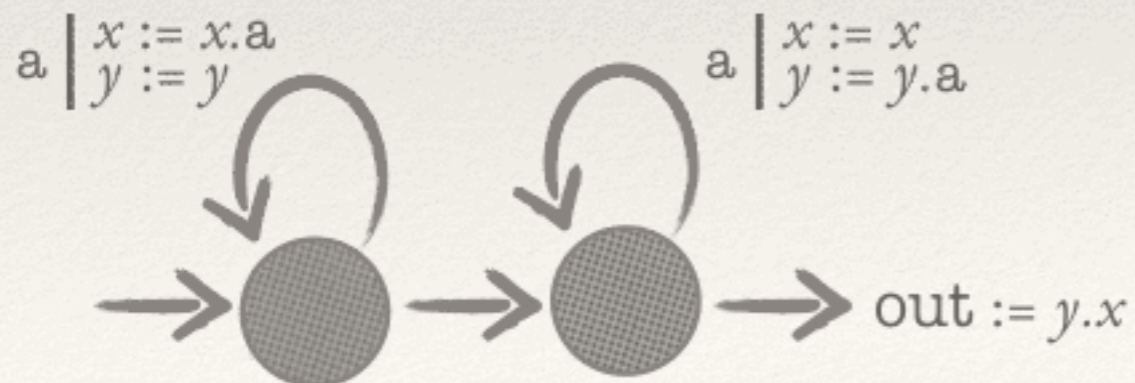
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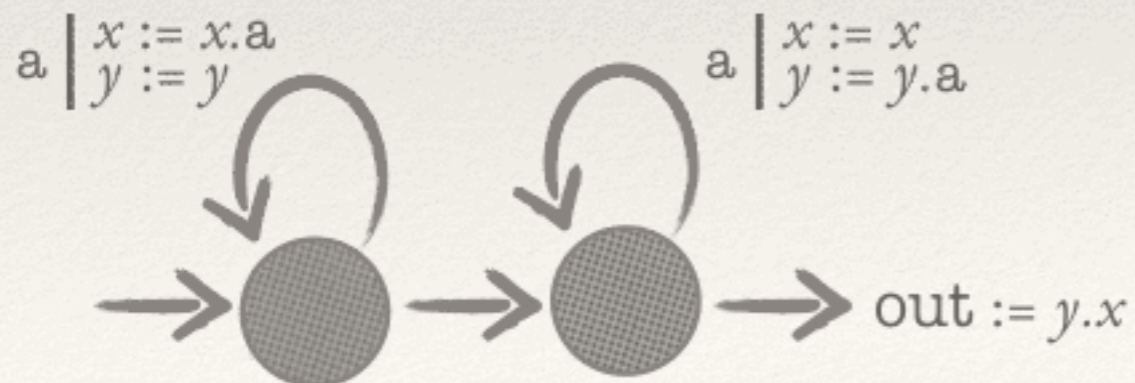
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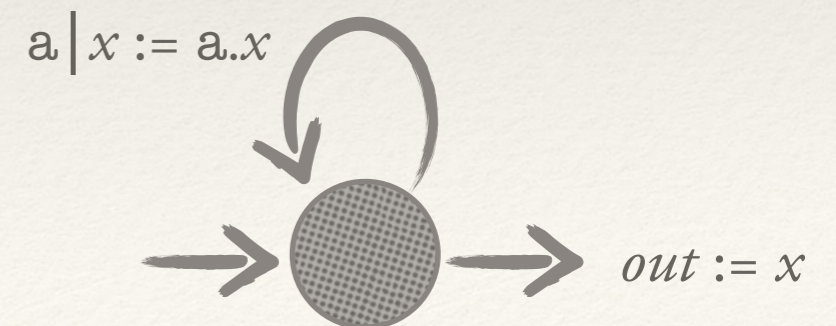
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- ❖ 1-way
- ❖ write-only **registers** to store partial outputs
+ **copyless restriction** = each register used at most once

E.g. split & swap

$x =$
 $y =$



E.g. mirror



1DFT

$$aw \mapsto wa$$

2DFT = DSST = MSOT

$$w \mapsto ww$$

1NFT

$$w \mapsto \Sigma^{|w|}$$

2NFT

$$w \mapsto w^*$$

$$uv \mapsto vu$$

NSST = NMSOT

1DFT

$$aw \mapsto wa$$

2DFT = DSST = MSOT

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decidable equivalence

undecidable equivalence

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decidable equivalence

undecidable equivalence

Anything interesting beyond functional transductions?

k-valued transductions = at most *k* outputs for each input

- ❖ decidable equivalence?
- ❖ correspondence with logic (e.g. MSO) ?
- ❖ equivalent models (e.g. 2-way vs SSTs) ?
- ❖ effective characterisations (e.g. 1-way definability) ?

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a unifying approach:

**Decomposition
Theorem**

For a suitable class
 \mathcal{C} of transducers:

“Every k -valued transducer $\in \mathcal{C}$ can be decomposed
into a finite union of functional transducers $\in \mathcal{C}$ ”

The decomposition theorem for $\mathcal{C} = \{ \text{1NFTs} \}$:



Every k -valued 1NFT is a finite union of functional 1NFTs.

[Weber '96, Sakarovitch - de Souza '08]

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Corollaries:

- ❖ decidable equivalence of k -valued 1NFTs
- ❖ k -valued 1NFTs = k -valued order-preserving MSO transductions



Unboundedly many runs with same output...

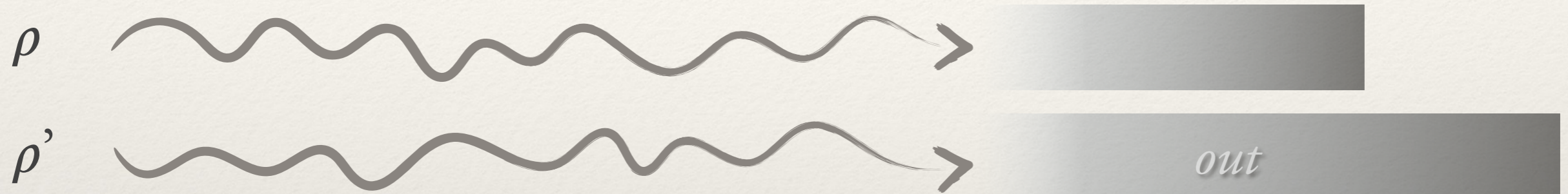


Follow lexico.-least run for each output

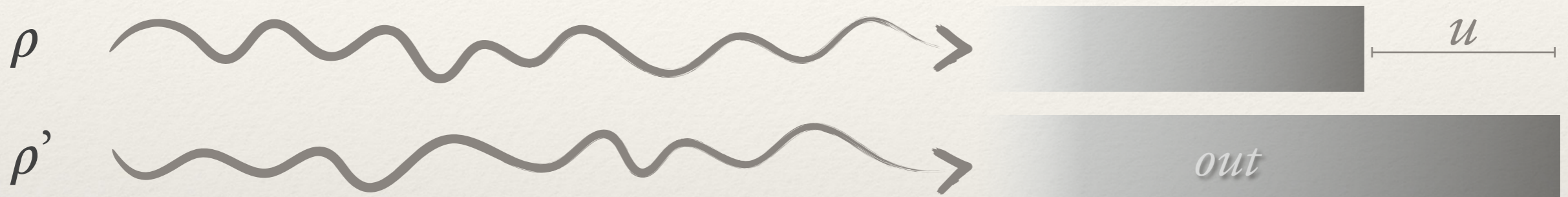


Subproblem: compare runs by their outputs

In a 1NFT, outputs are formed by appending only to the right

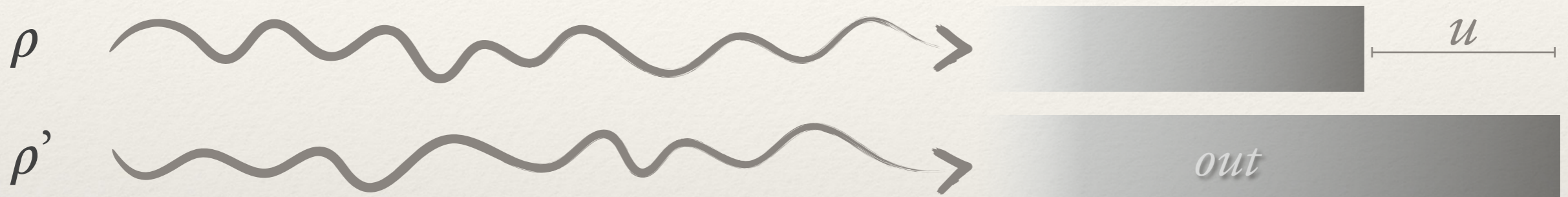


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$$\mathit{align}(\rho, \rho') = \begin{cases} (u, \varepsilon) : \mathit{out}(\rho) \cdot u = \mathit{out}(\rho') \\ (\varepsilon, v) : \mathit{out}(\rho) = \mathit{out}(\rho') \cdot v \end{cases}$$

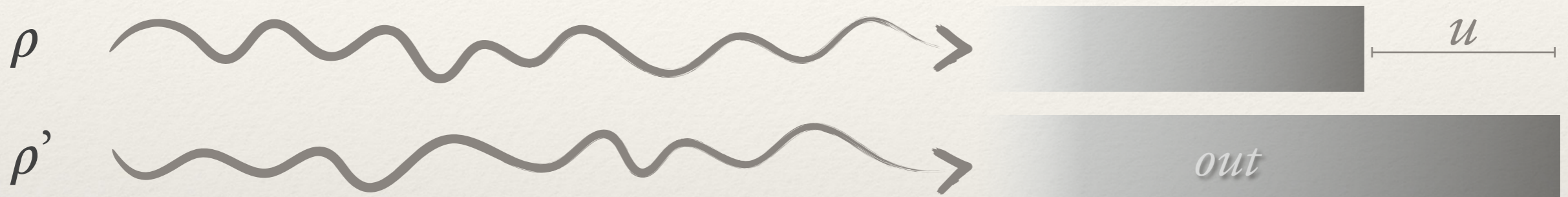
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$$\text{lag}(\rho, \rho') = |\text{align}(\rho, \rho')|$$

$$\text{maxlag}(\rho, \rho') = \text{MAX} \{ \text{lag}(\rho_{\leq t}, \rho'_{\leq t}) : t \leq |\rho| \}$$

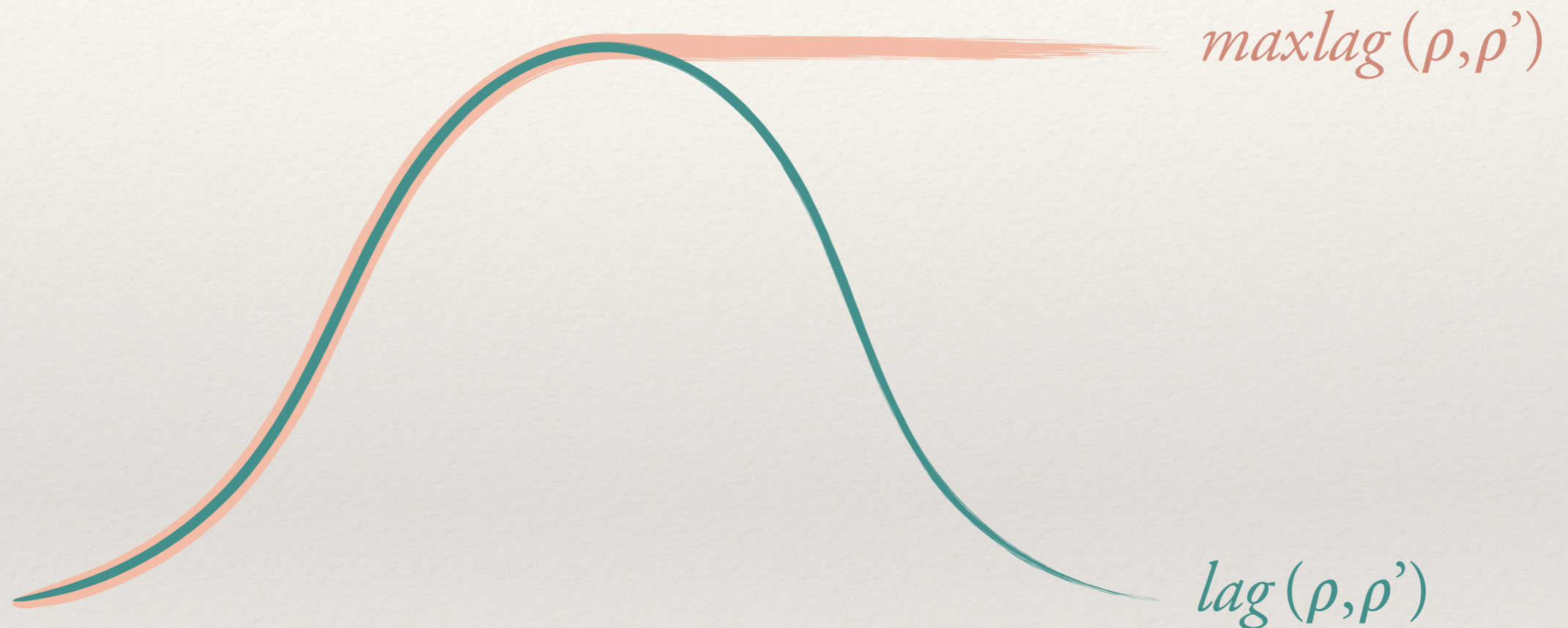
It may happen that $out(\rho) = out(\rho')$ yet with large $maxlag(\rho, \rho')$

$input = a a \dots a a \dots \# \dots a a \dots a a$

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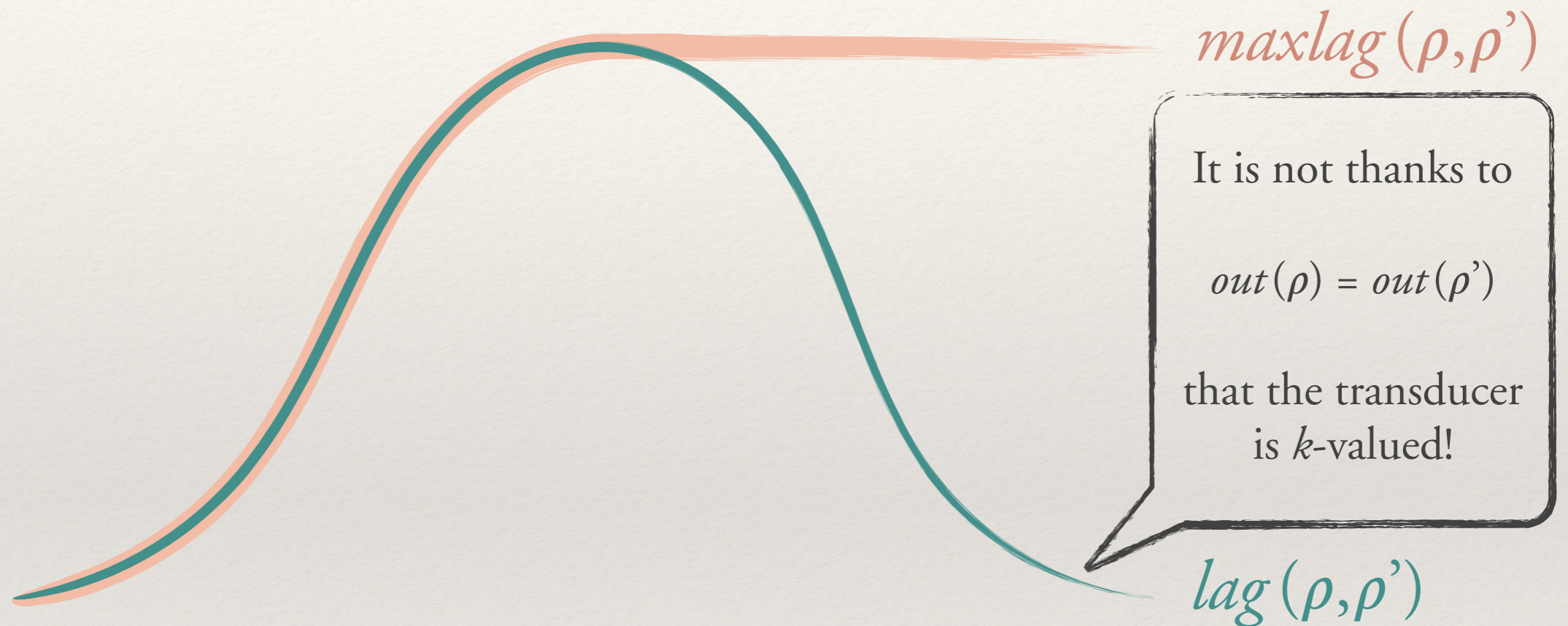


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input = a a ... a a ... # ... a a ... a a

out(ρ) = a a ... a a ...

out(ρ') = ... a a ... a a

Key combinatorial property:

$\rho_1, \dots, \rho_{k+1}$ runs
of k -valued 1NFT $\Rightarrow \exists i \neq j$ $out(\rho_i) = out(\rho_j)$ &
 $maxlag(\rho_i, \rho_j)$ small

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Moreover, if $maxlag(\rho_i, \rho_j)$ is small

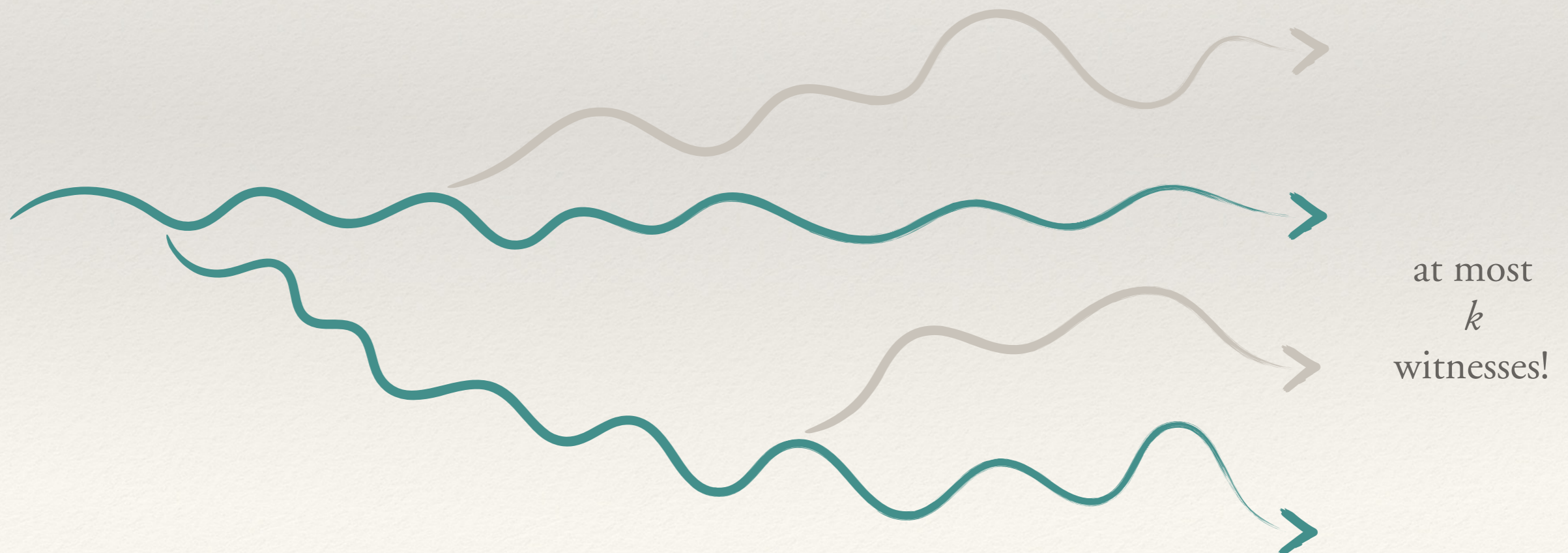
one can maintain $align(\rho_i, \rho_j)$ in bounded memory

— in particular, one knows whether $out(\rho_i) = out(\rho_j)$

One can simulate only the **witness runs**, namely, the ρ 's that are

✦ successful

✦ lexico.-least among all other runs ρ' with $\begin{cases} out(\rho) = out(\rho') \ \& \\ maxlag(\rho, \rho') \text{ small} \end{cases}$



Conjecture: every k -valued SST is a finite union of functional SSTs

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Some corollaries:

- ❖ decidable equivalence of k -valued SSTs [Alur, Deshmukh '11]
- ❖ k -valued SSTs = k -valued MSO transductions [Alur, Cerny '10]
= k -valued 2NFTs [Engelfriet, Hoogeboom '01]
- ❖ effective characterisation of k -valued SSTs
definable by k -valued 1NFTs [Filiot, Gauwin, Reynier, Servais '13]

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Some corollaries:

$T \subseteq T_1 \cup \dots \cup T_k$ decidable
for functional SSTs T, T_1, \dots, T_k

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[Alur, Deshmukh '11]

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in the functional case

DSSTs = MSO = 2NFTs

[Alur, Deshmukh '11]

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Our contribution: we proved the conjecture for SSTs with **1 register**

First difficulty: letters added to left and right of register \Rightarrow symmetric alignments on registers



$$\text{align}(\rho, \rho') = \left\{ \lambda = (u, v, w, z) : u \cdot \text{reg}(\rho) \cdot v = w \cdot \text{reg}(\rho') \cdot z \right\}$$

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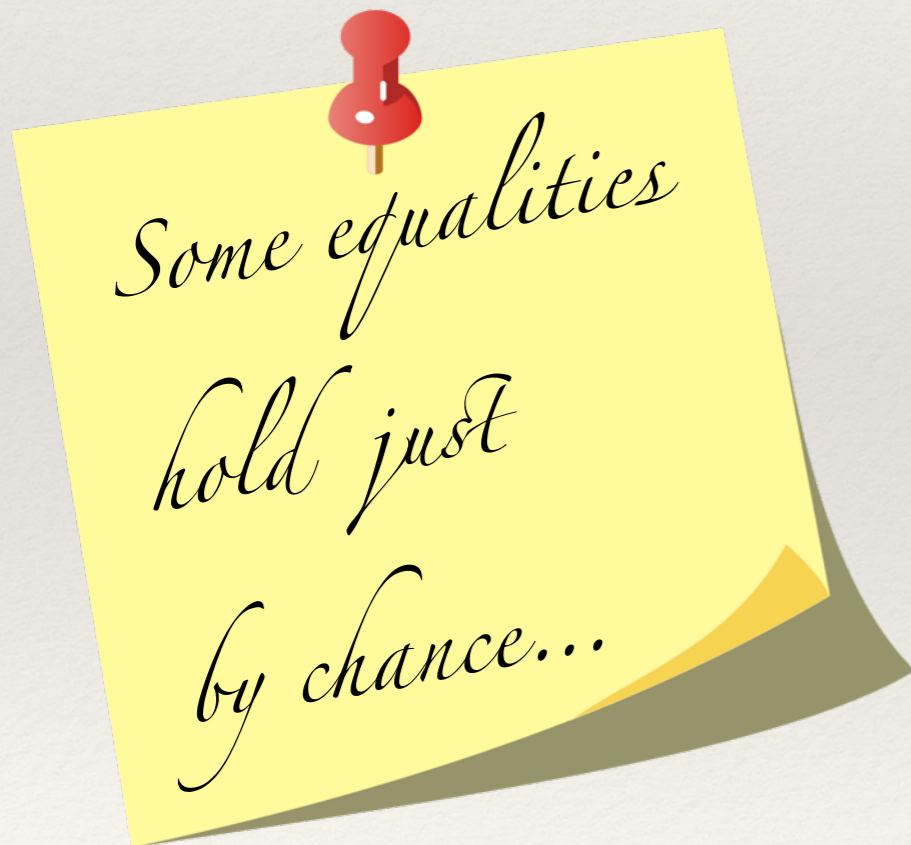
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$$\text{maxlag}(\rho, \rho') = \text{MAX} \left\{ \text{lag}(\rho_{\leq t}, \rho'_{\leq t}) : t \leq |\rho| \right\}$$

Second difficulty: combinatorial property

$k+1$ runs $\bar{\rho} = \rho_1, \dots, \rho_{k+1}$
of k -valued SST $\Rightarrow \exists i \neq j \quad \text{reg}(\rho_i) = \text{reg}(\rho_j) \ \& \ \text{maxlag}(\rho_i, \rho_j) \text{ small}$



Some equalities
hold just
by chance...

Decomposition of 1-register SSTs

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of k -valued SST $\Rightarrow \exists i \neq j$ $reg(\rho_i) = reg(\rho_j)$ &
 $maxlag(\rho_i, \rho_j)$ small

$(i, j) \in \text{Equals}(\bar{\rho})$

$(i, j) \in \text{SmallLags}(\bar{\rho})$

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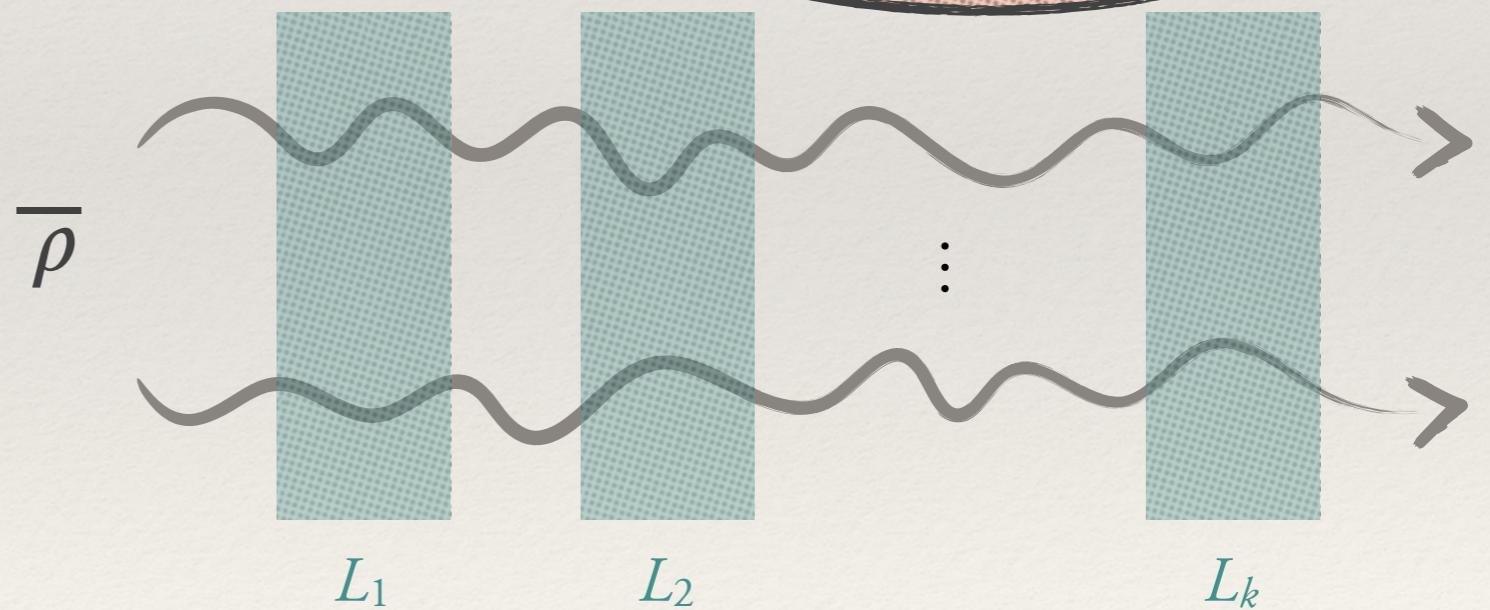


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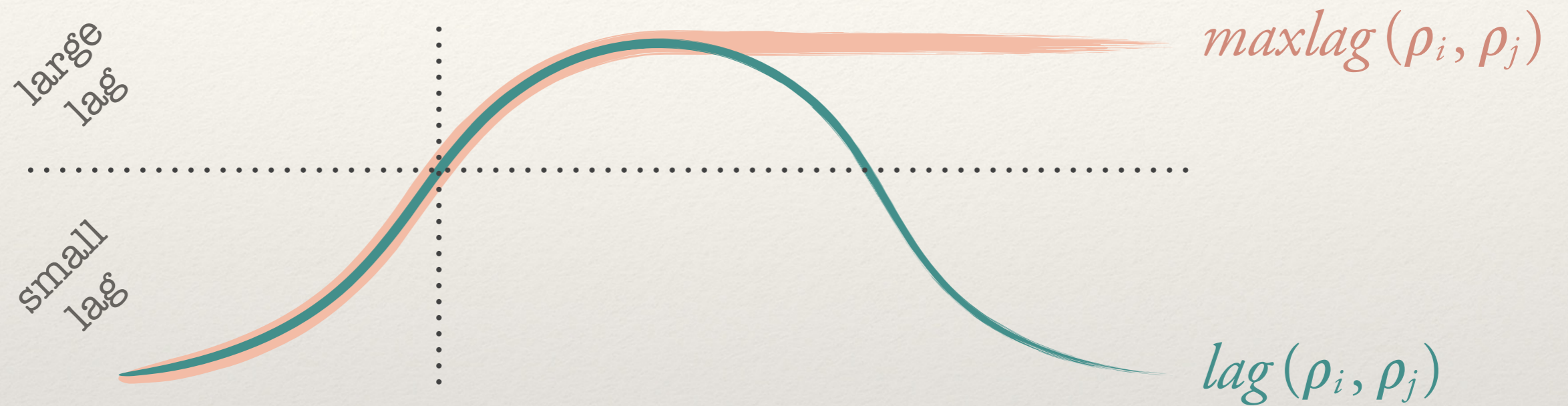
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$(i, j) \in \text{Equals}(\bar{\rho})$ is not robust to pumping

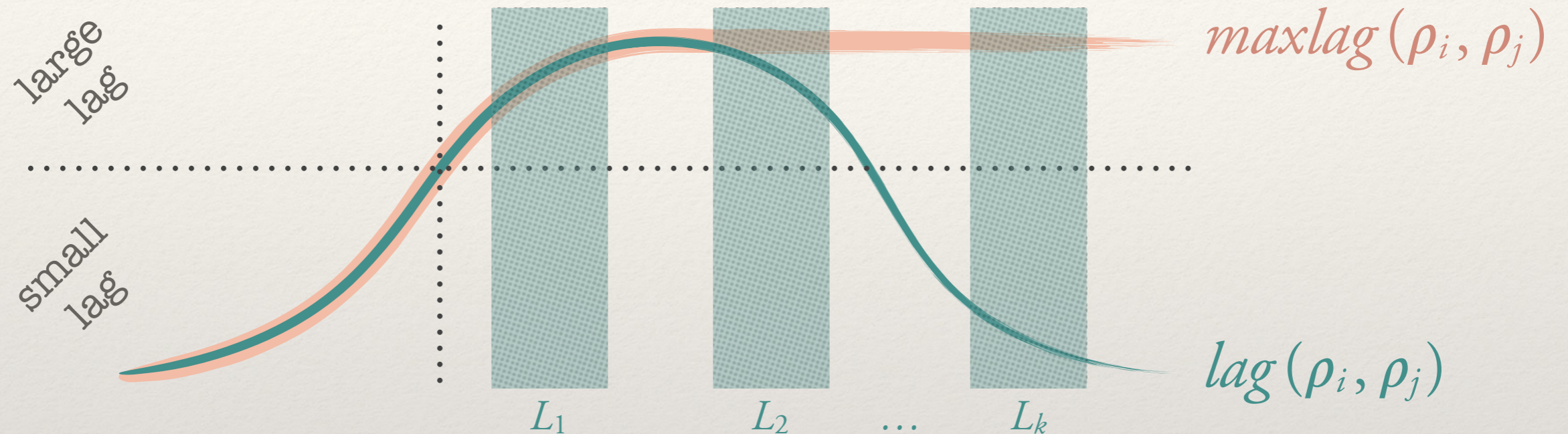
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Decomposition of 1-register SSTs

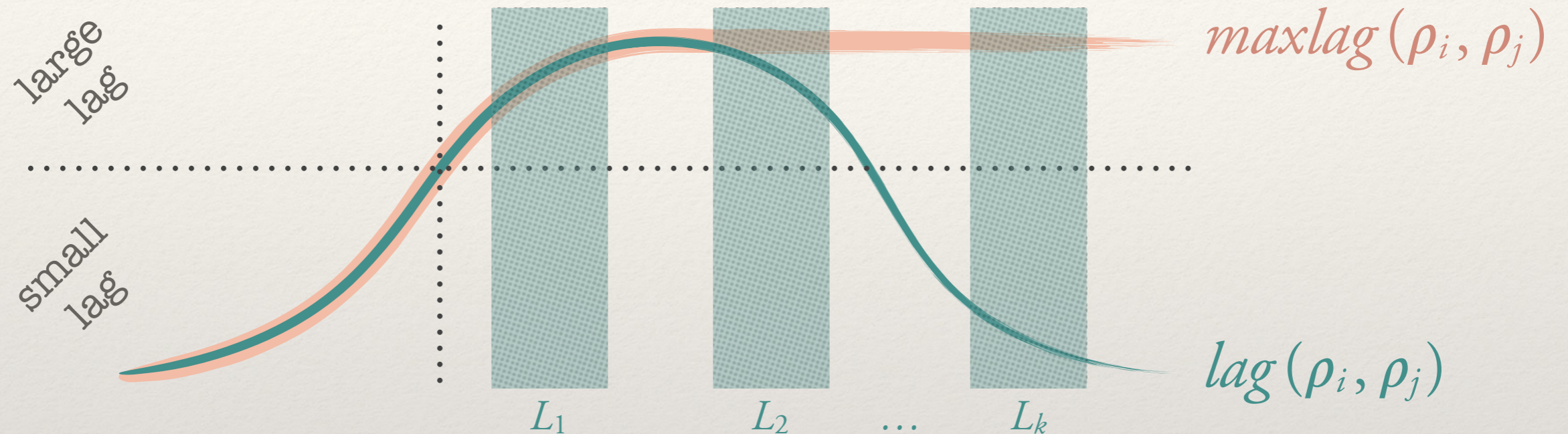
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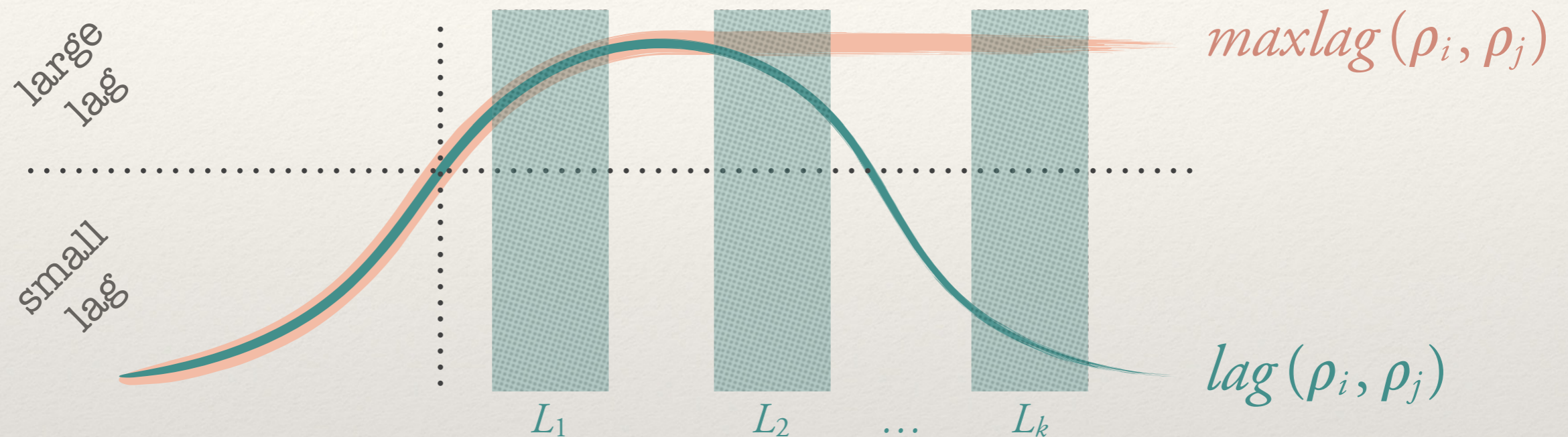
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- 2 cases
- $(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$ holds for only finitely many n 's
 - $(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$ holds for infinitely many n 's

Take $(i, j) \in \mathbf{Equals}(\overline{\rho})$

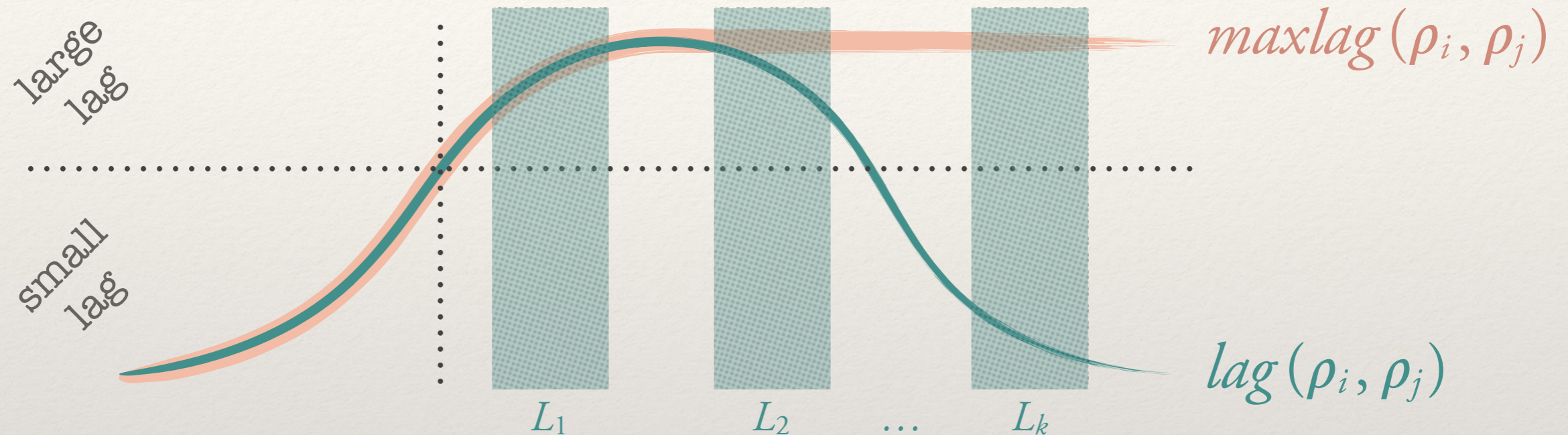
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- 2 cases
- $(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$ holds for only finitely many n 's
 \Rightarrow replace $\overline{\rho}$ by $\overline{\rho}_n$ for large enough n
 - $(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$ holds for infinitely many n 's

Take $(i, j) \in \mathbf{Equals}(\overline{\rho})$

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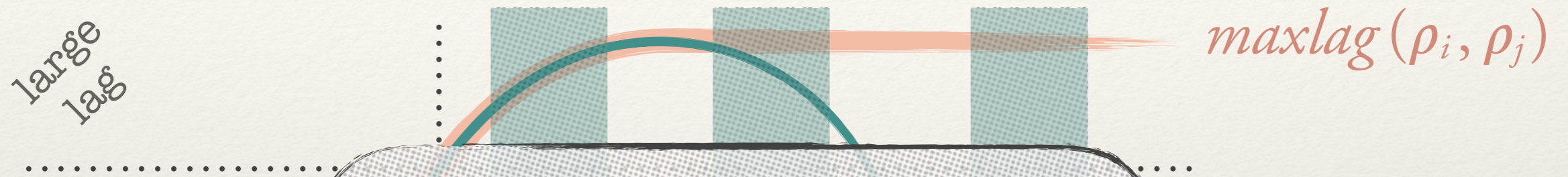
2 cases \rightarrow $(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$ holds for only finitely many n 's
 \Rightarrow replace $\overline{\rho}$ by $\overline{\rho}_n$ for large enough n

$(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$ holds for infinitely many n 's
 \Rightarrow it holds for all n 's, including $n=0 \dots$



Take $(i, j) \in \mathbf{Equals}(\overline{\rho})$

Let $\overline{\rho}_n = \mathit{pump}_{L_1, \dots, L_k}^n(\overline{\rho})$



Word equations of the form

$$u_0 (v_1)^n u_1 \dots (v_h)^n u_h = u'_0 (v'_1)^n u'_1 \dots (v'_h)^n u'_h$$

$\mathit{lag}(\rho_i, \rho_j)$

2 cases

$(i, j) \in \mathbf{Equals}(\overline{\rho})$

$(i, j) \in \mathbf{Equals}(\overline{\rho}_n)$



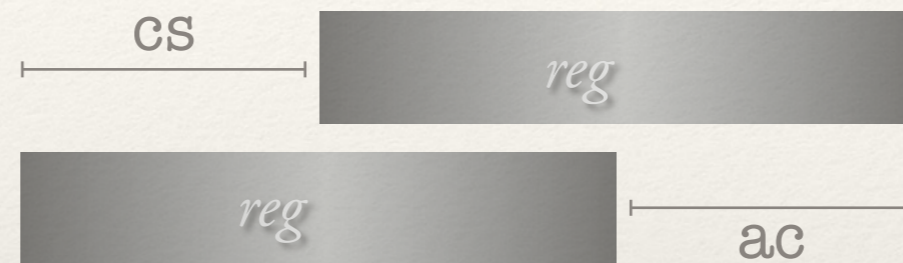
$\overline{\rho}_n$ for large enough n

finite

[Kortelainen '98, Saarela '15] \Rightarrow it holds for all n 's, including $n=0 \dots$



Third difficulty: maintain alignments in bounded memory



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After updating

$$reg(\rho) := reg(\rho) \cdot s$$

$$reg(\rho') := a \cdot reg(\rho')$$



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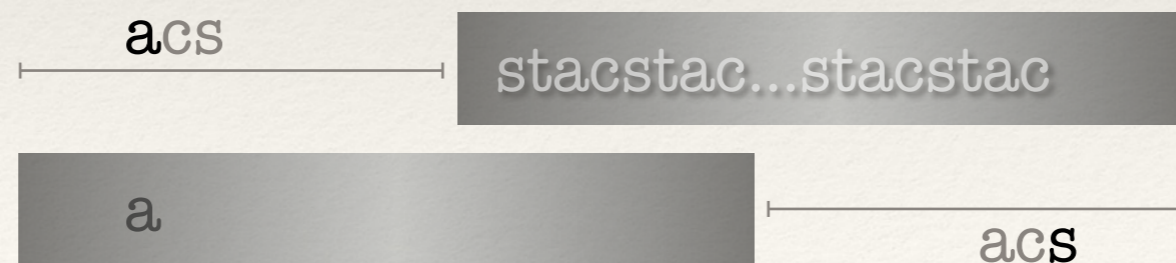
$$reg(\rho') := a \cdot reg(\rho')$$



+ knowledge on periodicity:

$$reg(\rho) \in \{ stac \}^*$$

$$reg(\rho') \in \{ csta \}^*$$



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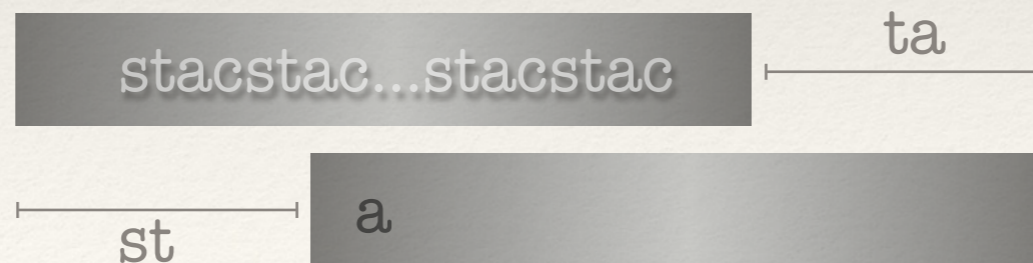
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+ knowledge on periodicity:

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Theorem

Every k -valued SST with 1 register is a union of k functional SSTs.

Corollary

Equivalence problem for k -valued SSTs with 1 register is decidable.

A first steps towards a decomposition theorem for SSTs with many registers...

Managed to prove the **combinatorial property** with many registers:

$\rho_1, \dots, \rho_{k+1}$ runs of k -valued SST	\Rightarrow	$\exists i \neq j$ $out(\rho_i) = out(\rho_j)$ & $maxlag(\rho_i, \rho_j)$ small
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Managed to prove the **combinatorial property** with many registers:

$$\rho_1, \dots, \rho_{k+1} \text{ runs of } k\text{-valued SST} \implies \exists i \neq j \quad \text{out}(\rho_i) = \text{out}(\rho_j) \ \& \ \text{maxlag}(\rho_i, \rho_j) \text{ small}$$

Idea:

1. not all loops induce repetitions of factors in the registers
2. those that do not induce repetitions can be simulated with less registers
3. word equations + induction on number of registers...

$\text{maxlag}(\rho_i, \rho_j)$ small

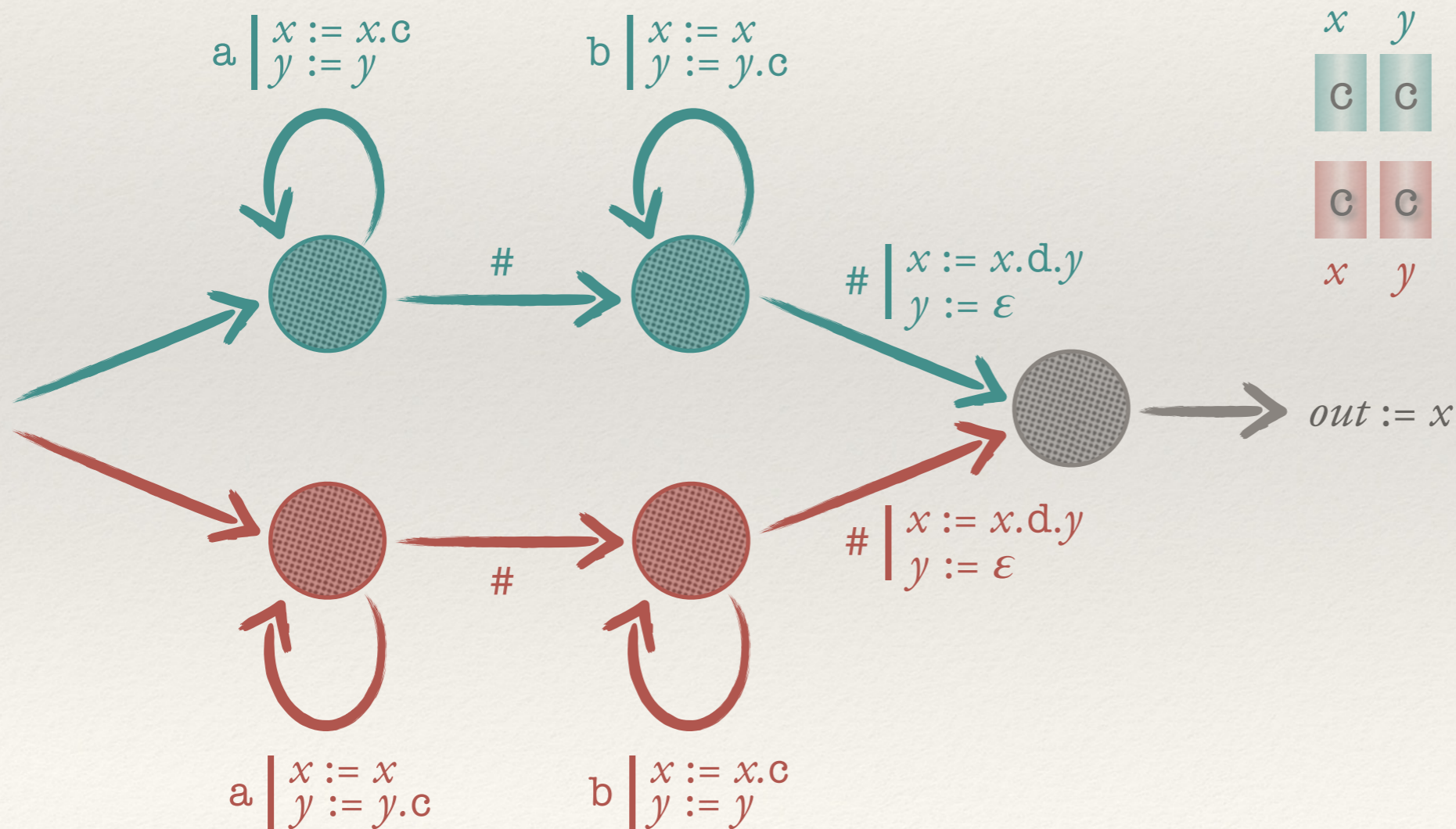


$\text{align}(\rho_i, \rho_j)$ maintainable
in bounded memory

$maxlag(\rho_i, \rho_j)$ small

FALSE

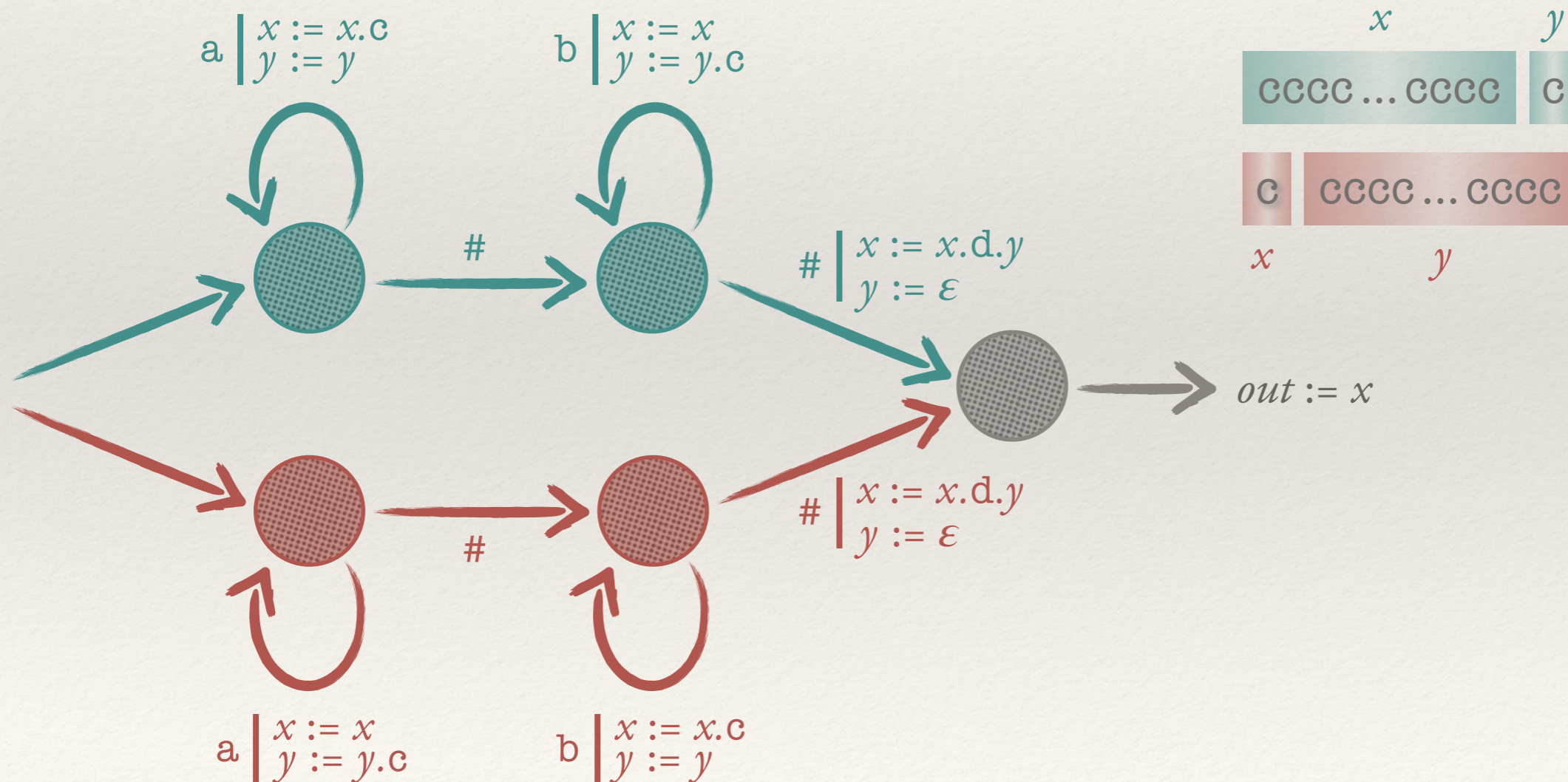
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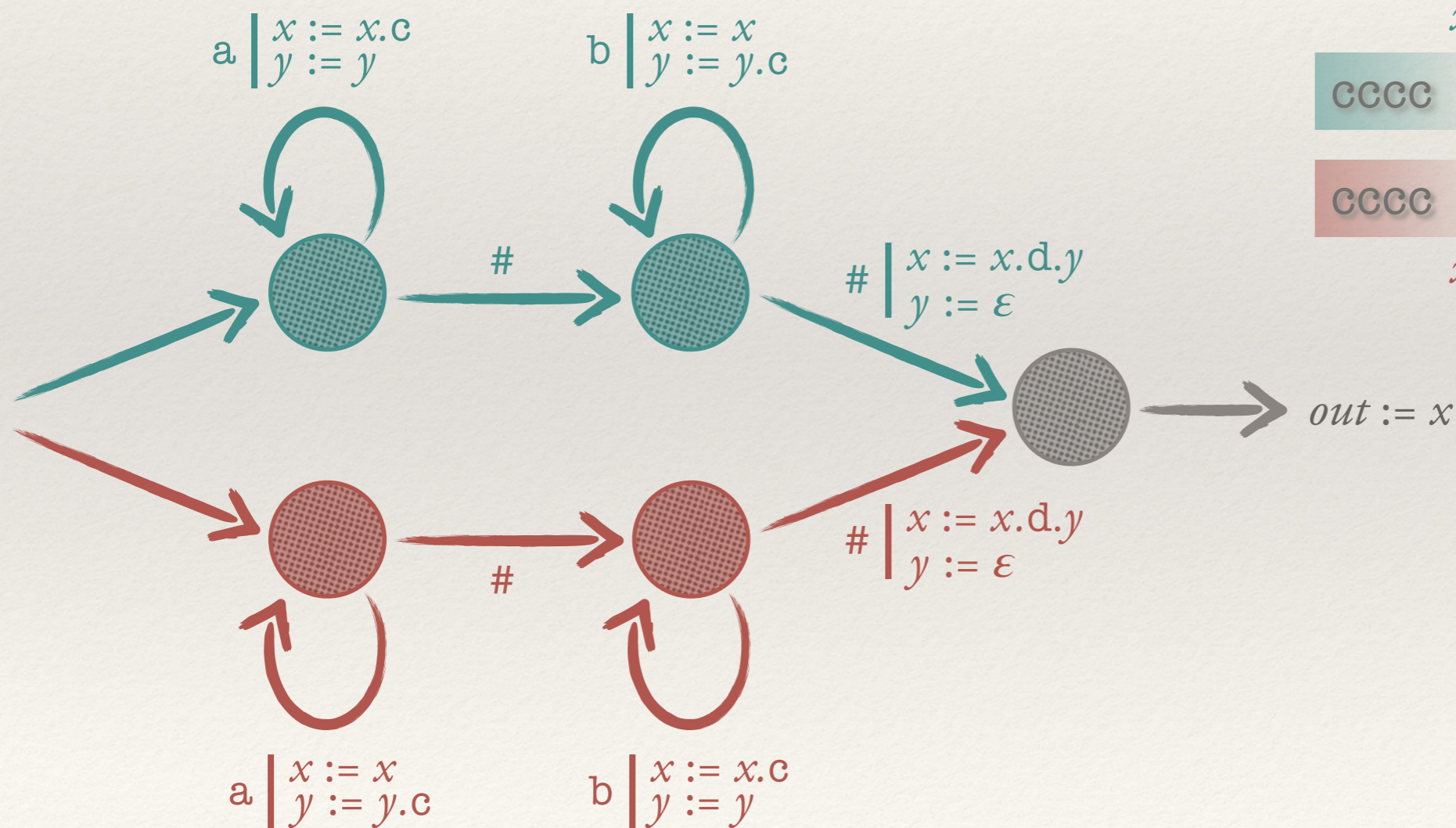
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